

n+ -> 18
p+ -> 14

Basics of electronics eng.

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1. Insulator has full valence band.
2. Adding impurity to the pure semiconductor is doping.
3. If the electron (CB) & hole (VB) are created by thermal excitation of a VB to the CB are called electron hole pair (EHP).
4. Atomic density of Si 5×10^{22} atoms/cm³.
5. No. of EHPs at 300K in Si is only about 10^{10} EHP/cm³.
6. For every \bar{e} moving with a given velocity, there is an equal & opp. motion.
6. Current density can be expressed as
In a unit volume.
7. In intrinsic material no. of \bar{e} is same as no. of holes.
8. In a p-type semiconductor \bar{e} are minority & Holes are majority.
9. In a p-type, we add trivalent impurity like Boron.
10. In n-type \bar{e} are majority & Holes are minority.
11. In an n-type we add pentavalent impurity like Phosphorus.
12. Direct semiconductor are used in LEDs & Semiconductor lasers.
13. Diode is a device which allows I in one direction. * 1 joule of energy is = to $1(1.6 \times 10^{-19})$ eV.

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* Charge of an e is 1.602×10^{-19} coulomb

fr.
1.97
4.6
3.
1.72

- B) Strain
- C) Deformation
- D) Defect

6 Series - A)

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A) Plastic Strain
Energy

Reverse volt -> constant
conductivities

depletion
region -> current \uparrow abruptly

24. What is the barrier volt of Si 0.7

25. In pn junction is reverse biased, depletion layer has only immobile +ve & -ve ions called transition capacitance / depletion region

26. This region is depleted of mobile charges & acts like a dielectric with fixed spn charges at both ends of the dep. layer called depletion region

27. Who acts electrodes in depletion region capacitor & are conducting. p-region & n-region

28. When reverse volt $\propto \uparrow$ thickness of the dep. layer is \uparrow & mobile charges are less

29. Abrupt change from acceptor ions in the p-region to donor ions in the n-region. N_A & N_D are unequal are step graded junction

20. Depletion region capacitance of step graded junction is inversely proportional to square root of the reverse volt.

19. When pn junction is FB, potential barrier \downarrow is called Diffusion capacitance

20. Its charge carrier density is high with \uparrow volt the \uparrow in FB volt

20. Ratio of change in stored charge outside depletion region to the change in applied voltage is known as Diffusion capacitance or storage capacitance

21. diffusion capacitance is \propto to the forward current of the diode

Expt. No.

23. Rectifier is a device convert Ac to dc
24. In Centre tap full wave rectifier efficiency is high
25. after rectifier we use capacitor for smoothing
26. Zener diode is a heavily doped specially designed to operate in the breakdown region.
27. who invented Zener diode Clarence Melvin Zener used as a volt. regulation;
28. P-n junction diode is lightly doped & has thick depletion region
29. Zener diode has thin depletion region.
30. P-n junction diode has weak electric field
31. Zener diode has strong electric field
32. In P-n junction diode here occurs avalanche breakdown collision & has high voltage & P-n junction destroys
33. Zener diode doesn't generate heat & doesn't destroy & has low volt.
34. knee voltage for Ge is 0.3V & for Si is 0.7V
35. who invent BJT William Shockley in 1952
36. In BJT charge carriers are Holes & electrons
37. emitter is heavily doped & has moderate size
38. Base is lightly doped & has thin
39. collector is moderate doped & has large size
40. In active region emitter is F.B & collector is & used for amplifier
41. Saturation region $a \rightarrow$ F.B, c \rightarrow F.B short circuit of AC switch ON
42. cut off \rightarrow O.P.C.T or switch off R.B. R.B

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43. CC has High $I_{impedance}$, low output I_m , CB Low (I_m) High (I_m), CE Medium, medium.

44. volt gain in CC NO CB Yes CE Yes
 & current gain in CC Yes CB No CE Yes
 & power gain in CC medium CB medi. CE High.

45. Phase shift in CC 0° CB 0° CE 180°

46. CC is used in Impedance matching CB High frequency applic. CE audio frequency cation.

largest current CC CB CE
 larger than CB

CC HI
 CB LI
 CE HI
 CC - no
 CB - yes
 CE - no

CC HI
 CB LI
 CE HI
 CC - no
 CB - yes
 CE - no

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LES

Current
in one direction from
higher to

Bridge	Centre
diode	2.
Volt drop high	low
Good Volt	Better Volt

PIV V_m $2V_m$
Transformer utilization factor 0.816 factor 0.816

Diode forward resist R_f $2R_f$
 $\eta = 0.816$ $\eta = 0.816$

ripple factor 0.40 0.40

form factor 1.11 1.11

peak factor $\sqrt{2}$ $\sqrt{2}$

resistor

O/P frequency $2f$ $2f$

Full wave
 $\eta = \frac{0.816}{1.11}$

form factor 1.11

ripple factor 0.40
DC power out $\frac{I_m^2}{\pi^2}$
 $\frac{I_m}{\pi} \times 0.692$

rec. $\eta = 0.816$

PIV $\rightarrow 2V_s$

2 diode

half wave

2.57

1.11

TUF 0.207

rect. $\eta = 0.6$

VS
2 diode

F	FF
1.11	1.97
0.816	4.06
0.40	1.81
$\frac{I_m}{\pi}$	$\frac{I_m}{\pi^2}$
2.57	